

Below is the NetBSD GSoC [questionnaire](#) that I have answered so that we are in the same page.

About the project:

What I have understood (please correct me if I am wrong) is, that the current implementation for ARM processors is a BIOS one. Our NetBSD fellows have written an x86 implementation of UEFI Bootloader for x86-64 machines. Now you would like to have that same implementation ported over to ARM platform with x86 specific instructions replaced with ARM instructions.

What will be the deliverables of the project?

Code, Documentation, Bug Fixes in future and a happy mentor ☺

Give an overview of how you intend to reach the project's goal in the form of milestones and a schedule:

Goal will be to complete the primary milestones asap. Weekly discussion and progress reports to my mentor. Still need to work out the schedule with mentor...

Is similar software already available elsewhere?

Yes. Most of the software we'll need is available.

Is the project a port of software, or a rewrite?

The project is outside of kernel and userland since it's a Bootloader. The Bootloader port and rewrite of NetBSD's x86 EFI bootloader. X86 specific instructions will be replaced with their ARM equivalent instruction(s).

What interfaces in NetBSD will your project use?

The UEFI is the interface here between the HW and SW. The FDT will be relevant as a data structure.

To what degree are you familiar with those interfaces?

I have read about the BIOS but not very proficient with UEFI. I have read its Wikipedia page though. I am still reading up on ARM's FDT.